

NAME

ghoti - circuit purifier for extracted sea-of-gates layouts

SYNOPSIS

ghoti [-options] <cell_name>

OPTIONS

<cell_name>

Nelsis circuit to be purified.

-h Help: print list of options.

-c Do not remove badly connected cmos transistors.

-i Do not remove cmos isolation transistors.

-p Do not remove badly connected resistors and capacitors.

-u Also remove totally unconnected instances of any sort.

-s Also remove partially unconnected instances of any sort.

-n <number>

Perform <number> preprocessing steps (defaults to 2).

-P Do not join power and ground nets.

-q Quiet option: print nothing except errors.

-z Print zstatistics about hash table usage.

-r Reduce series/parallel networks of transistors.

-o <out_name>

Write output to cell name.

-C Do not require <cell_name> to start with a capital.

-D Debug: print graph.

-M Print memory usage.

-W Debug: no write.

DESCRIPTION

Ghoti is a netlist purifier, it purifies the Nelsis circuit view of a cell. It is used to remove unconnected transistors from a Sea-of-gates design after the layout to circuit extraction with the *space*(ICD) program. This is needed before starting an *s/s*(ICD) simulation.

EXAMPLE

To purify the circuit of the cell dcfclock use:

```
% ghoti -i dcfclock
```

AUTHORS

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FILES

proj_dir/image.seadif (technology file)