

NAME

autocheck - do a single layer check

SYNOPSIS

autocheck [-f] [-t] [-g] [-v] [cell_name]

OPTIONS

- t** Debugging option, do not filter or merge error messages for a non orthogonal layout.
- g** Report gap errors between elements of the same group.
- f** Use file 'dimcheckdata1' from the current working directory as design_rule file instead of the standard one.
- v** verbose mode

DESCRIPTION

Autocheck checks the layers of a cell definition on design rule errors. Width and gap checks are performed. Gap errors between edges of the same group are usually suppressed, use the -g option if this is not desired. The errors are reported on the standard output.

The program *autocheck* can be used after *exp*.

If no cell_name is specified the cells to be checked are read from the file "exp_dat".

The current working directory must be the project directory.

AUTHOR

T.G.R. van Leuken, J. Fokkema

FILES

ICDPATH/share/lib/dimcheck
executable

ICDPATH/share/lib/process/TECHN/dimcheckdata1
(TECHN=technology directory)

NELSISPROJECT/exp_dat
names of cells to be checked

NELSISPROJECT/layout/cell/LC_vln
input files, LC=LayerCode

SEE ALSO

T.G.R. van Leuken, J. Liedorp "An Hierarchical and Technology Independent Design Rule Checker", Delft University of Technology,
dimcheck(1ICD), dimdata(4ICD), exp(1ICD).

BUGS

It is understood that the design rules are consistent. Error reporting is rather poor; the kind of error (width or gap) is reported together with the coordinates of the two points between which the error occurred. In case of non orthogonal layouts the errors reported may be shifted a little from the place where they occurred due to rounding errors.